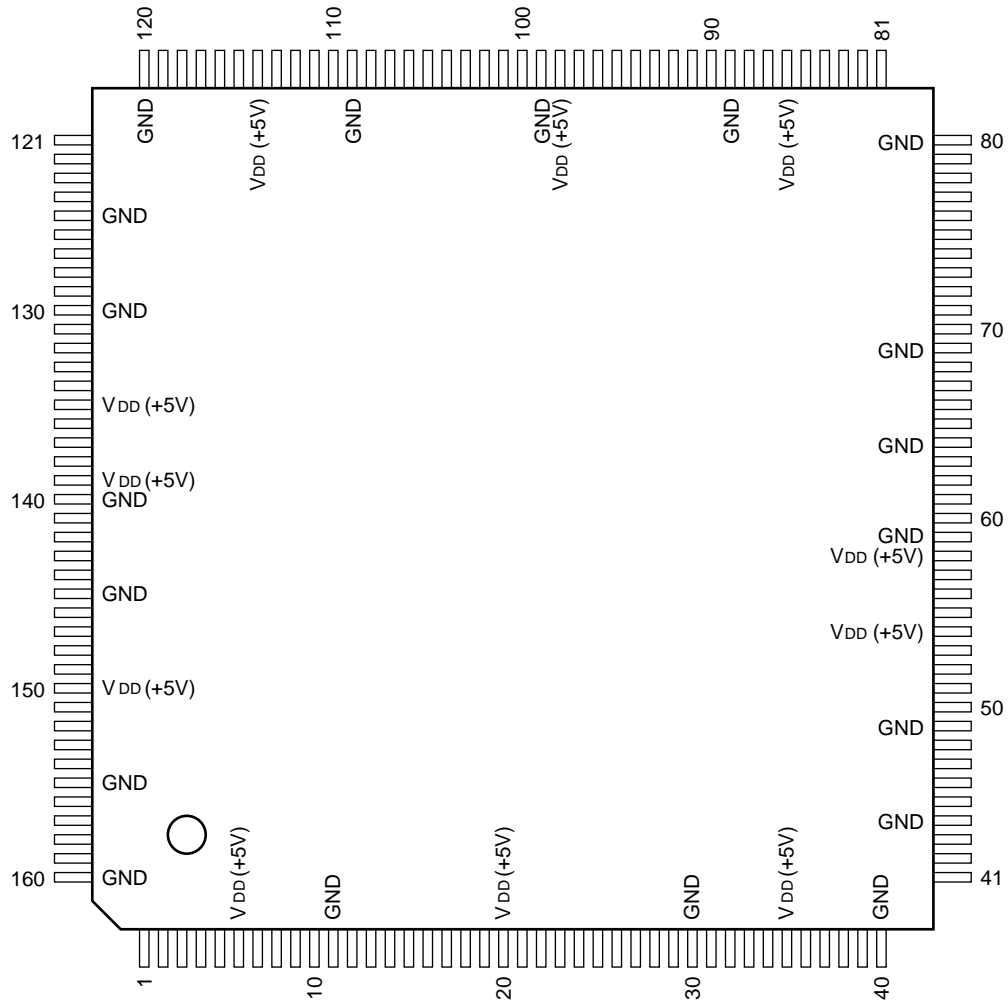

C-MOS FIELD-PROGRAMMABLE GATE ARRAY - TOP VIEW -



(V_{DD} = +5V)

PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	I/O	IO1	41	I/O	IO35	81	I/O	IO64	121	I/O	IO98
2	I/O	DCLK/IO2	42	I/O	IO36	82	I/O	IO65	122	I/O	IO99
3	I/O	IO3	43	I/O	IO37	83	I/O	IO66	123	I/O	IO100
4	I/O	IO4	44	–	GND	84	I/O	IO67	124	I/O	IO101
5	I/O	IO5	45	I/O	IO38	85	I/O	IO68	125	–	GND
6	–	V _{DD}	46	I/O	IO39	86	–	V _{DD}	126	I/O	IO102
7	I/O	IO6	47	I/O	IO40	87	I/O	IO69	127	I/O	IO103
8	I/O	IO7	48	I/O	IO41	88	I/O	IO70	128	I/O	IO104
9	I/O	IO8	49	–	GND	89	–	GND	129	I/O	IO105
10	I/O	IO9	50	I/O	IO42	90	I/O	IO71	130	–	GND
11	–	GND	51	I/O	IO43	91	I/O	IO72	131	I/O	IO106
12	I/O	IO10	52	I/O	IO44	92	I/O	IO73	132	I/O	IO107
13	I/O	IO11	53	I/O	IO45	93	I/O	IO74	133	I/O	IO108
14	I/O	IO12	54	–	V _{DD}	94	I/O	IO75	134	I/O	IO109
15	I/O	IO13	55	I/O	IO46	95	I/O	IO76	135	–	V _{DD}
16	I/O	PRB/IO14	56	I/O	IO47	96	I/O	IO77	136	I/O	IO110
17	I/O	IO15	57	–	V _{SV}	97	I/O	IO78	137	I/O	IO111
18	I/O	CLKB/IO16	58	–	V _{DD}	98	–	V _{DD}	138	–	V _{SV}
19	I/O	IO17	59	–	GND	99	–	GND	139	–	V _{DD}
20	–	V _{DD}	60	–	V _{PP}	100	I/O	IO79	140	–	GND
21	I/O	CLKA/IO18	61	–	V _{KS}	101	I/O	IO80	141	I/O	IO112
22	I/O	IO19	62	I/O	IO48	102	I/O	IO81	142	I/O	IO113
23	I/O	PRA/IO20	63	I/O	IO49	103	I/O	IO82	143	I/O	IO114
24	I/O	IO21	64	–	GND	104	I/O	IO83	144	I/O	IO115
25	I/O	IO22	65	I/O	IO50	105	I/O	IO84	145	–	GND
26	I/O	IO23	66	I/O	IO51	106	I/O	IO85	146	I/O	IO116
27	I/O	IO24	67	I/O	IO52	107	I/O	IO86	147	I/O	IO117
28	I/O	IO25	68	I/O	IO53	108	I/O	IO87	148	I/O	IO118
29	I/O	IO26	69	–	GND	109	–	GND	149	I/O	IO119
30	–	GND	70	I/O	IO54	110	I/O	IO88	150	–	V _{DD}
31	I/O	IO27	71	I/O	IO55	111	I/O	IO89	151	I/O	IO120
32	I/O	IO28	72	I/O	IO56	112	I/O	IO90	152	I/O	IO121
33	I/O	IO29	73	I/O	IO57	113	I/O	IO91	153	I/O	IO122
34	I/O	IO30	74	I/O	IO58	114	–	V _{DD}	154	I/O	IO123
35	–	V _{DD}	75	I/O	IO59	115	I/O	IO92	155	–	GND
36	I/O	IO31	76	I/O	IO60	116	I/O	IO93	156	I/O	IO124
37	I/O	IO32	77	I/O	IO61	117	I/O	IO94	157	I/O	IO125
38	I/O	SDI/IO33	78	I/O	IO62	118	I/O	IO95	158	I/O	IO126
39	I/O	IO34	79	I/O	IO63	119	I/O	IO96	159	I	MODE
40	–	GND	80	–	GND	120	I/O	IO97	160	–	GND

CLKA, CLKB ; CLOCK INPUTS
 DCLK ; DIAGNOSTIC CLOCK INPUT (MULTIFUNCTION PIN)
 IO1-IO126 ; STANDARD I/Os (IO2, IO14, IO20, IO33 ; MULTIFUNCTION PINS)
 MODE ; MULTIFUNCTION PINS CONTROL INPUT
 PRA, PRB ; DIAGNOSTIC PROBE OUTPUTS (MULTIFUNCTION PINS)
 SDI ; SERIAL DATA INPUT FOR DIAGNOSTIC PROBE (MULTIFUNCTION PIN)

MULTIFUNCTION PINS CONTROL

MODE	MULTIFUNCTION PIN NO.				
	2	16	23	38	
0	IO2	IO14	IO20	IO33	
1	DCLK	PRA	PRB	SDI	

0 ; LOW LEVEL

1 ; HIGH LEVEL

V_{PP} , V_{SV} ; PROGRAMMING VOLTAGE INPUT.
 THESE PINS MUST BE CONNECTED TO V_{DD} LEVEL EXCEPT DURING PROGRAMMING.

V_{KS} ; PROGRAMMING VOLTAGE INPUT.
 THIS PIN MUST BE CONNECTED TO GND LEVEL EXCEPT DURING PROGRAMMING.